

Patented Dec 10, 1930

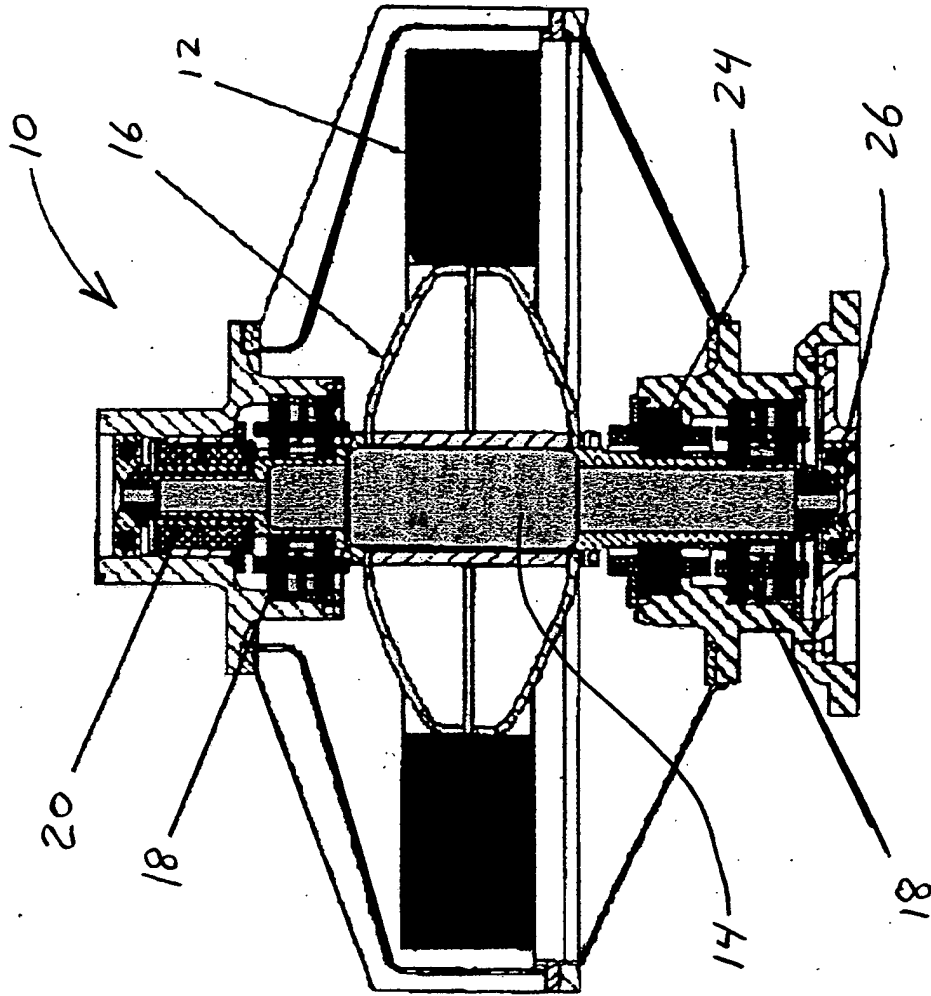
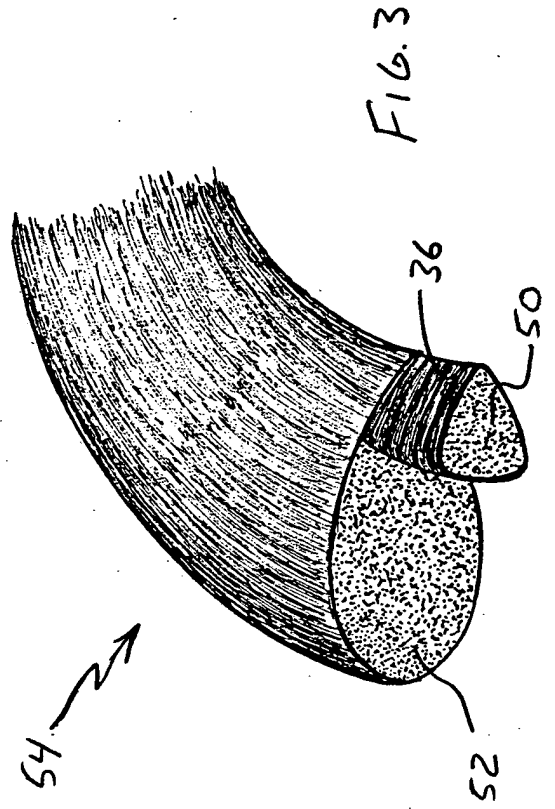
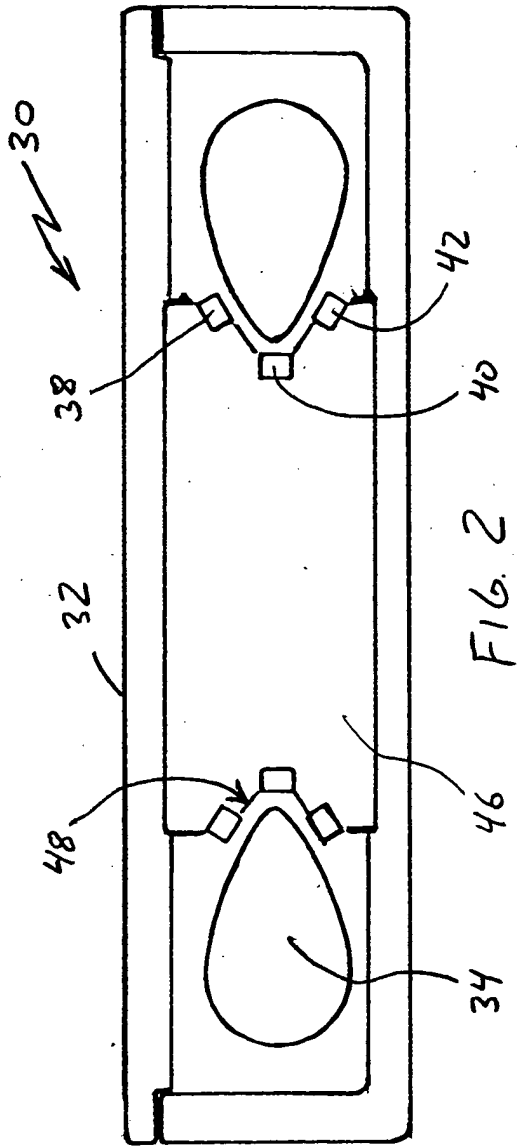


Fig. 1  
(PRIOR ART)



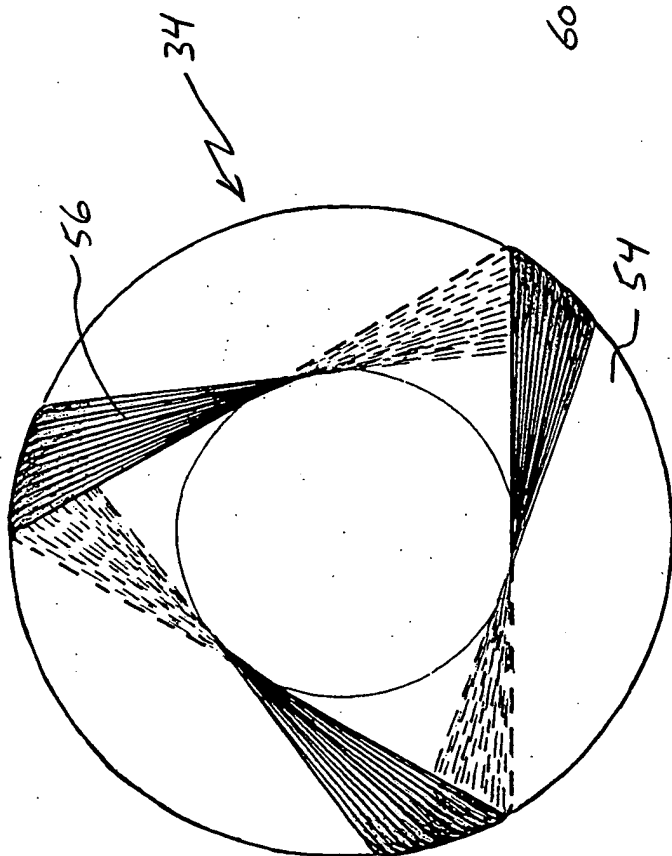


FIG. 4a

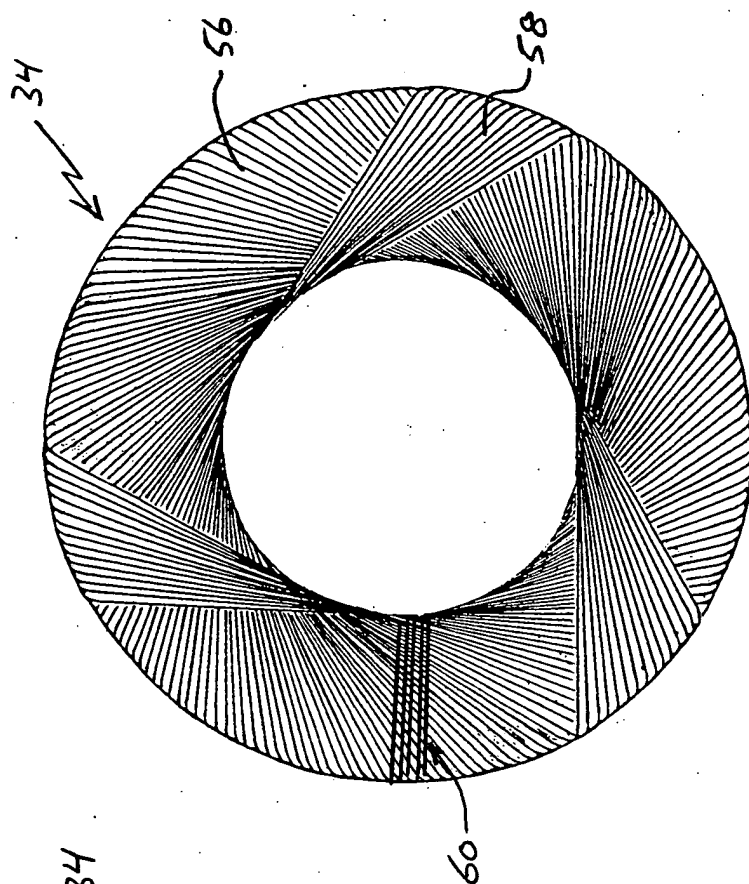


FIG. 4c

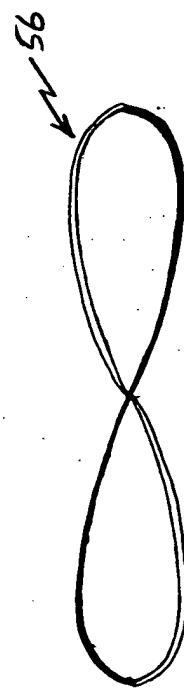


FIG. 4b

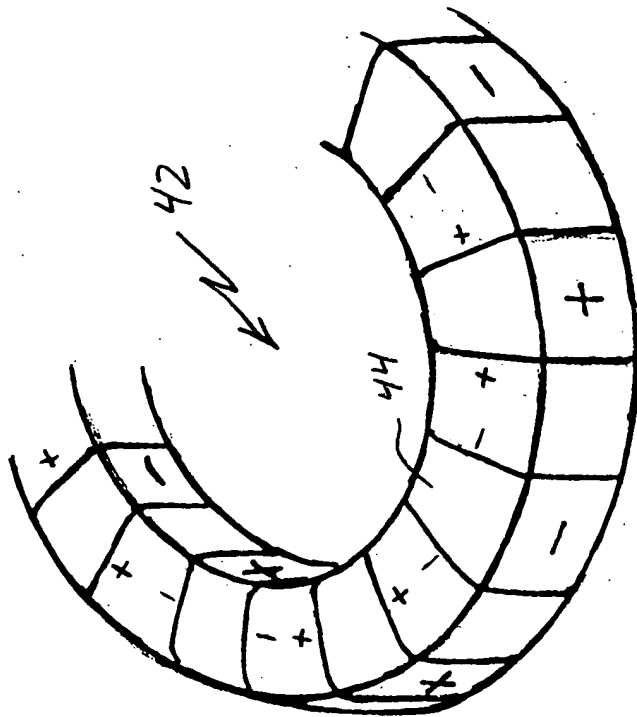


FIG. 5b

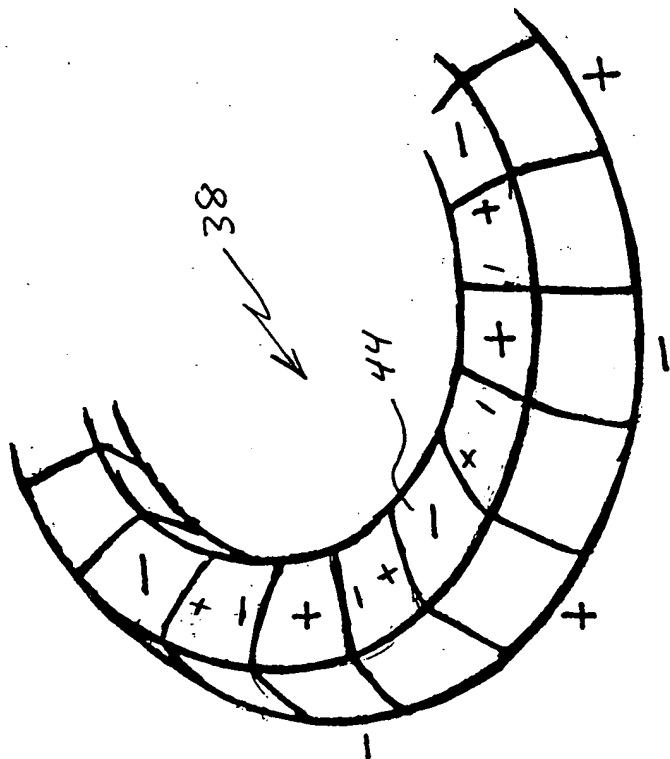
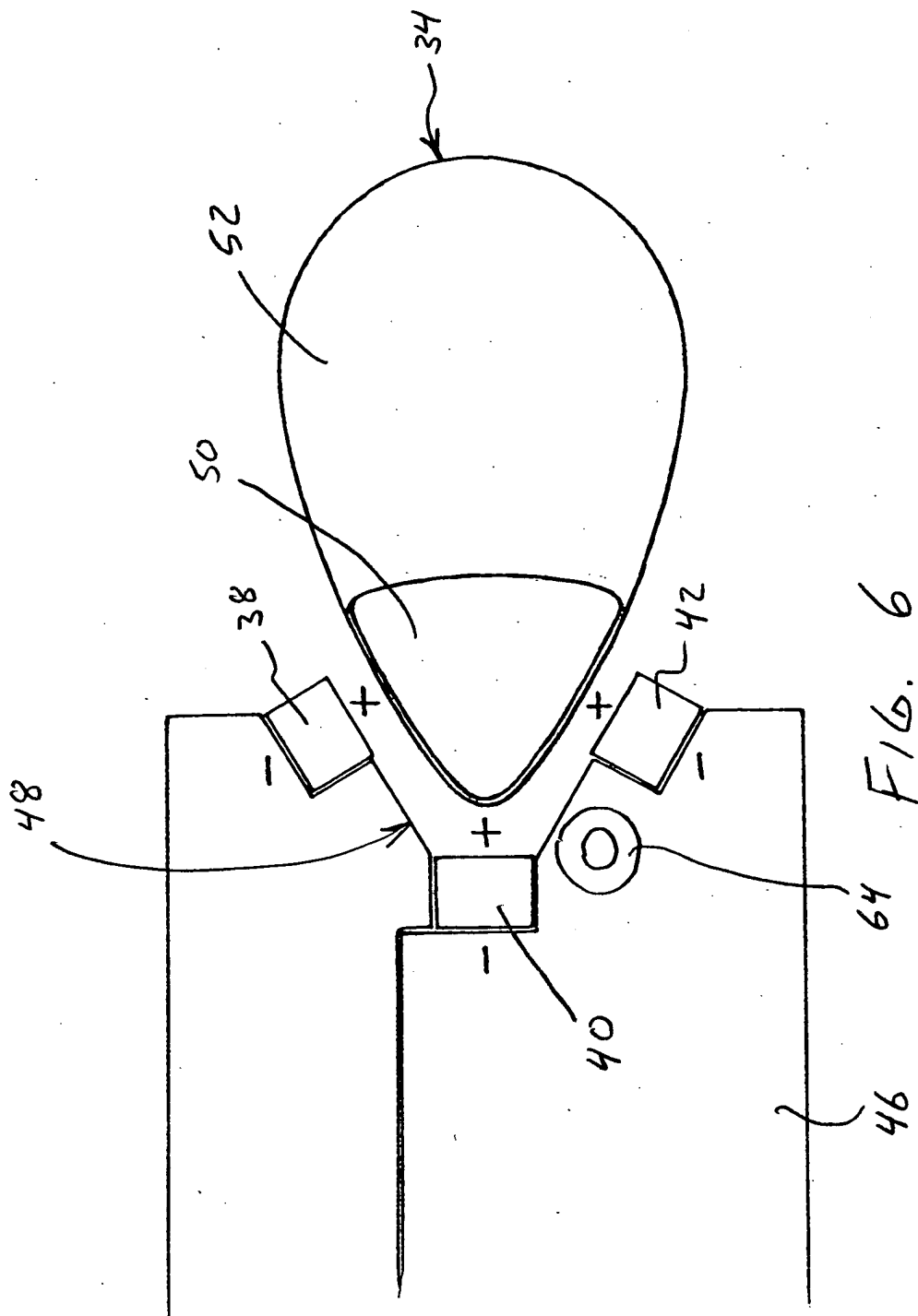


FIG. 5a



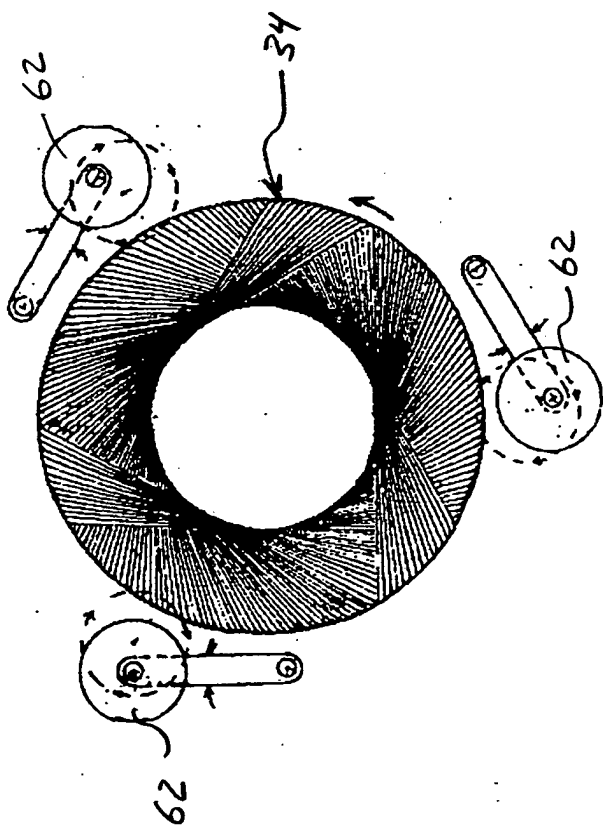


FIG. 7

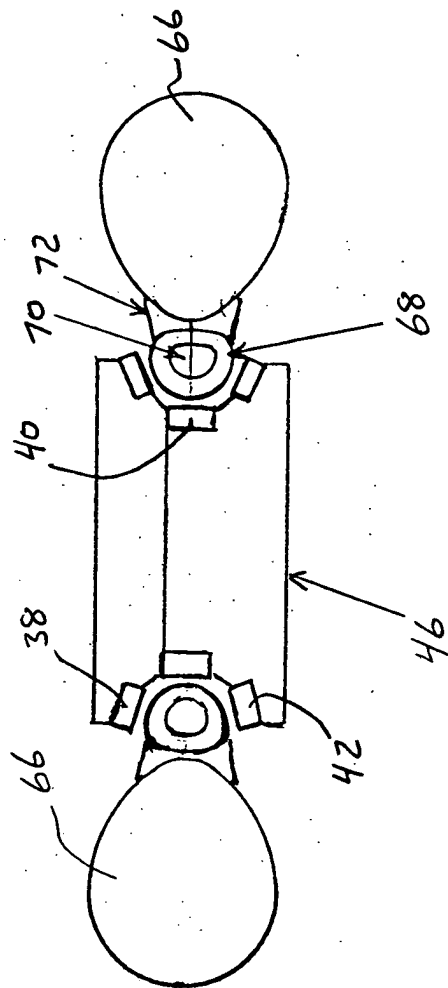


FIG. 8

FIG. 9 is a cross-sectional view of the device of FIG. 8, taken along line 9-9 of FIG. 8, showing the internal structure of the device, including the substrate 40, the gate 82, the source 84, the drain 86, the channel 88, and the contacts 90.

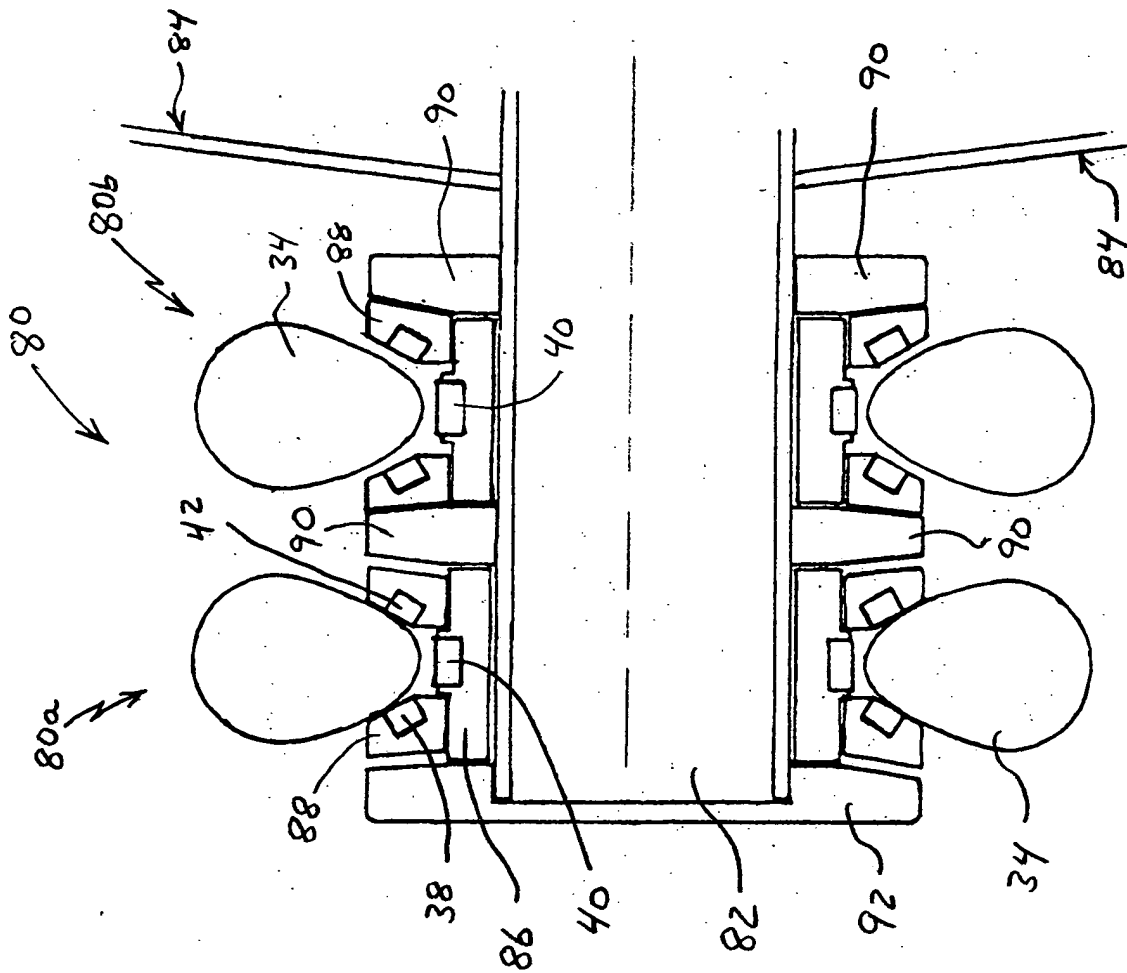


FIG. 9

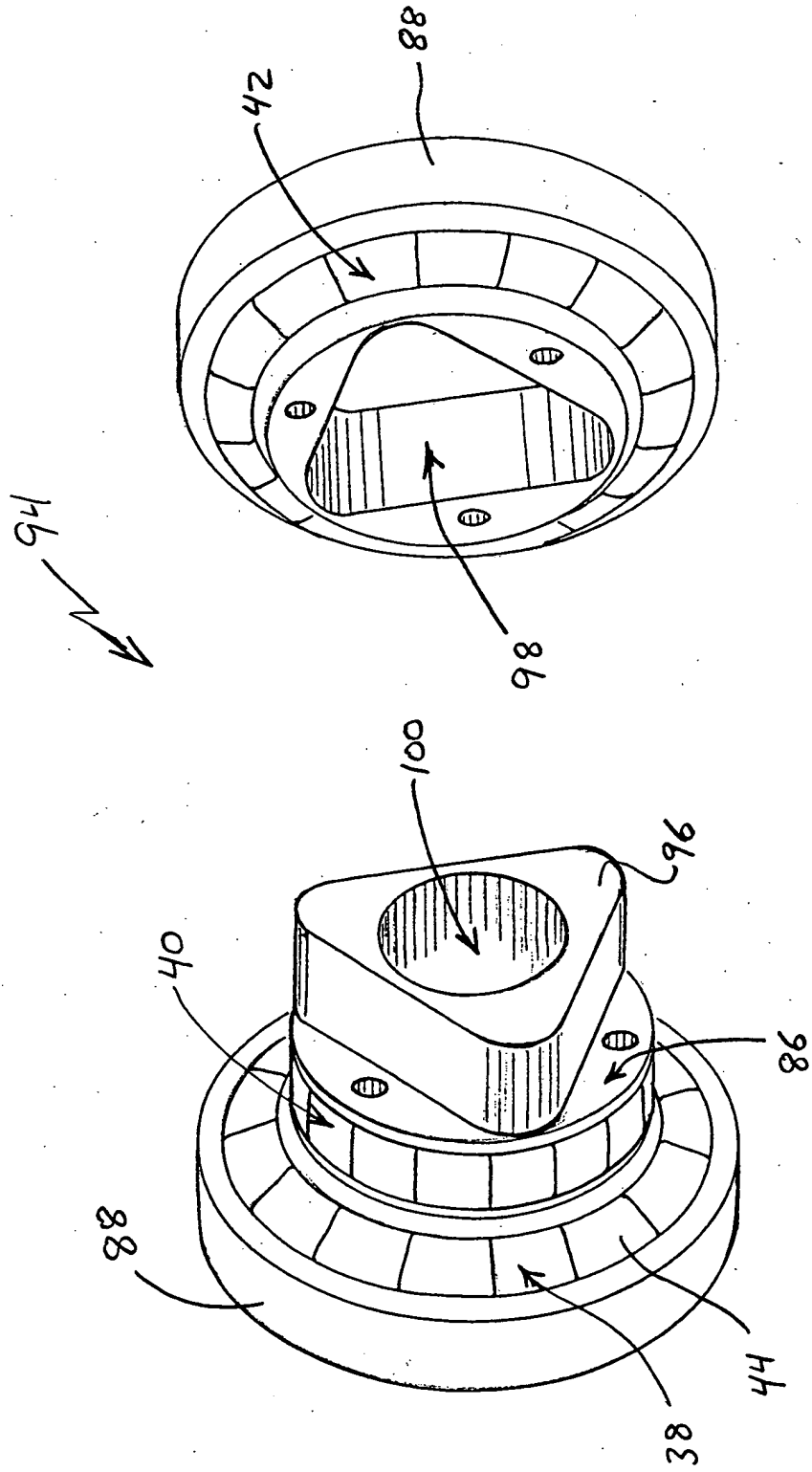


FIG. 10